

FIG. 1

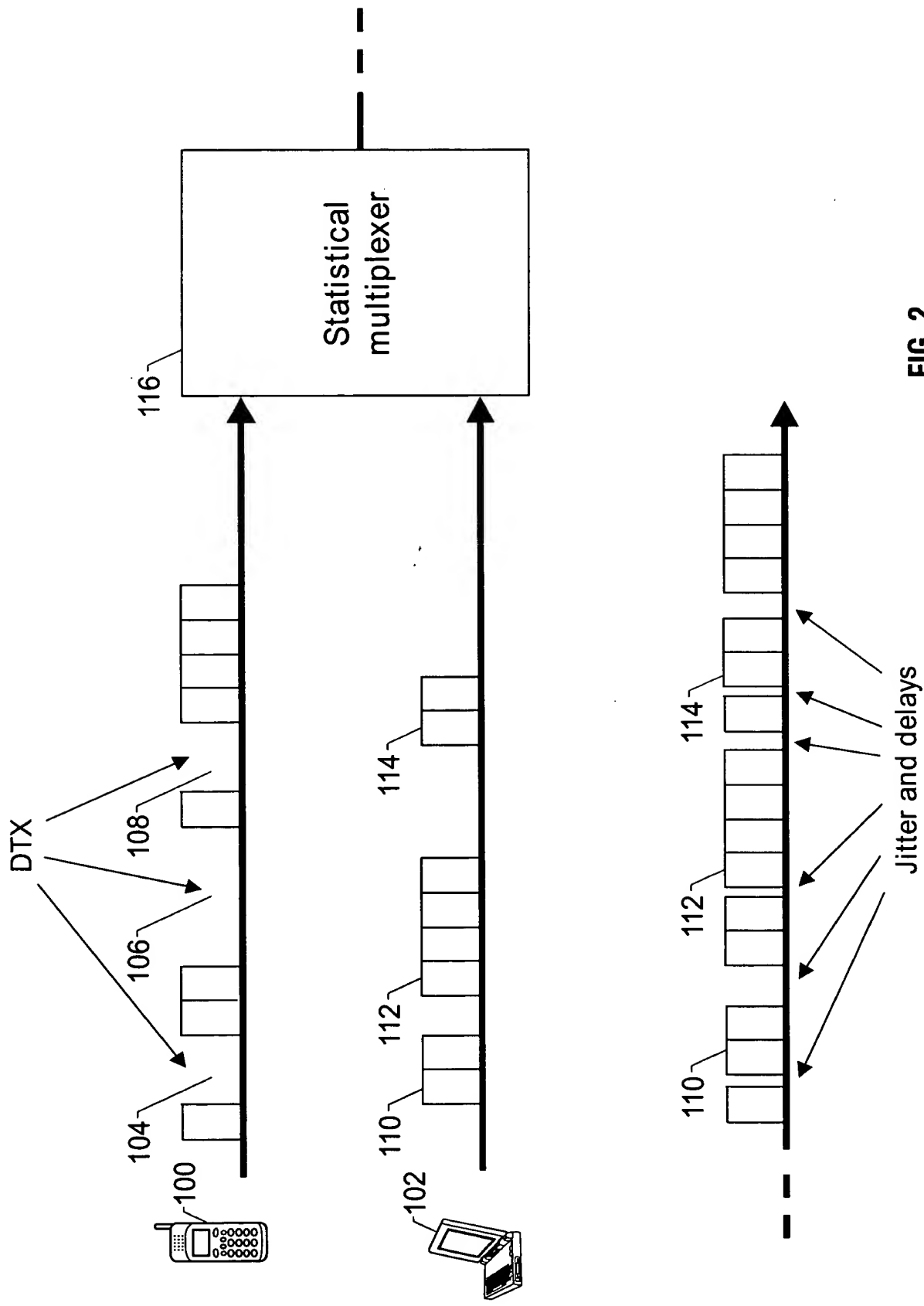


FIG. 2

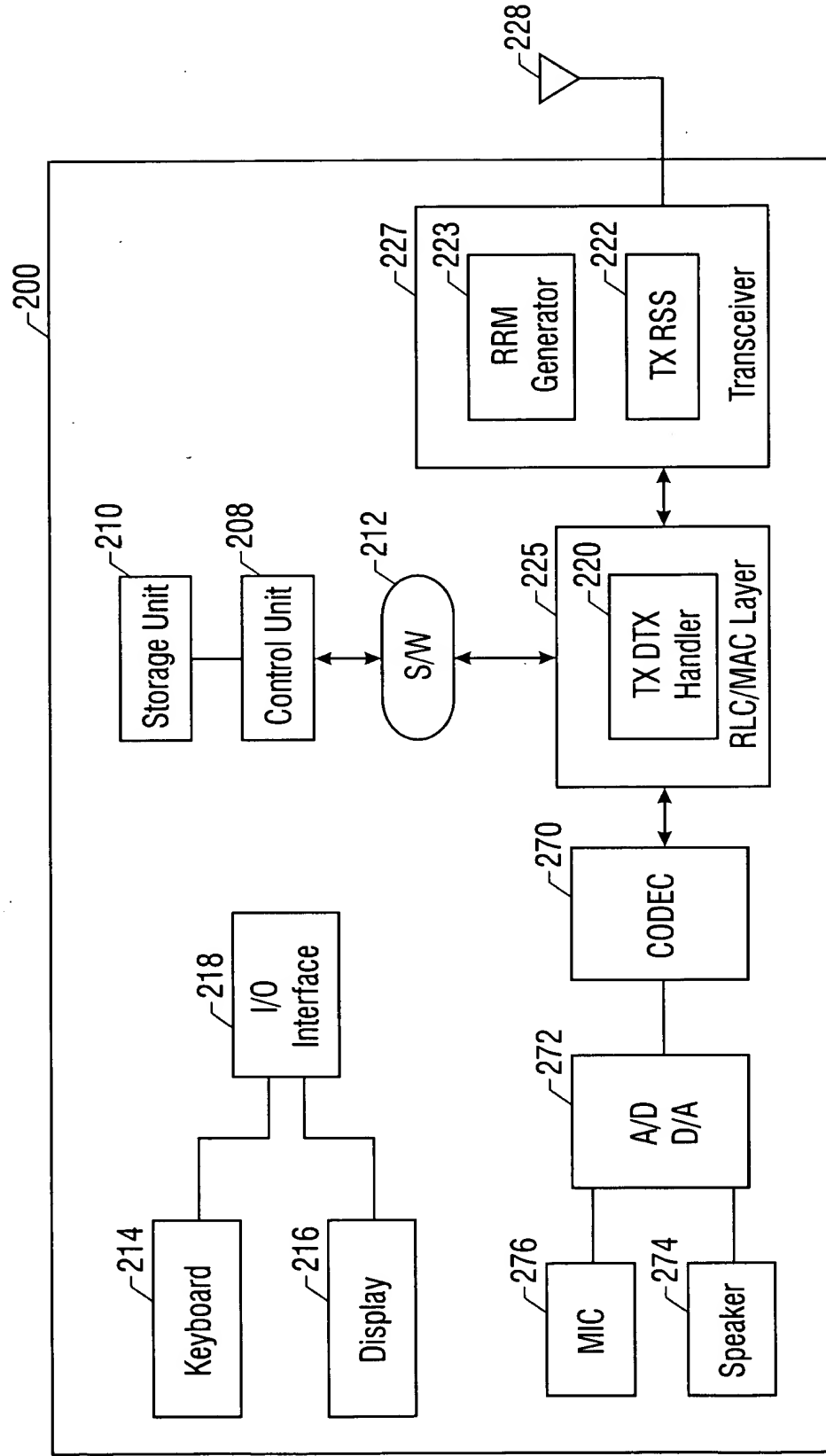


FIG. 4A

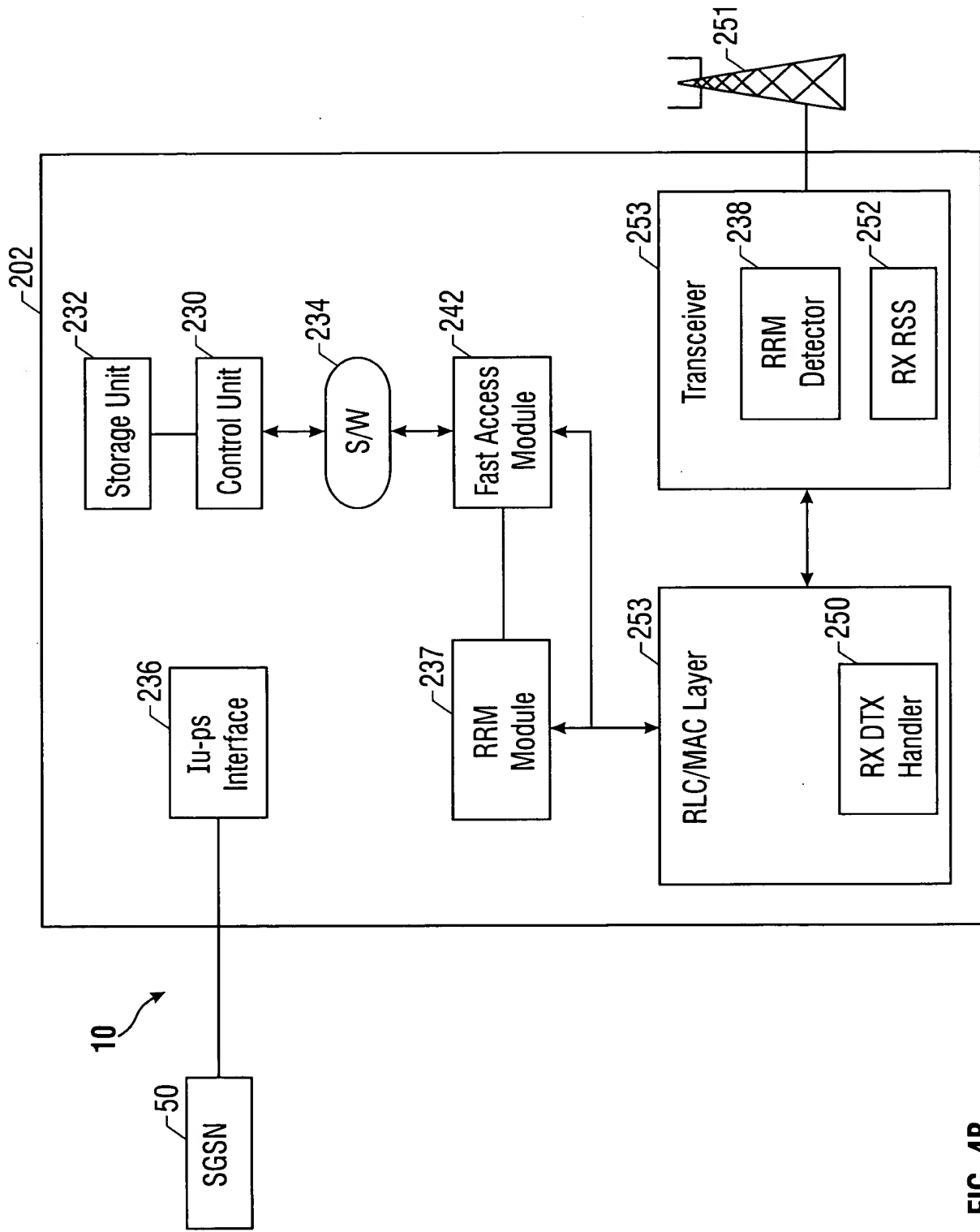


FIG. 4B

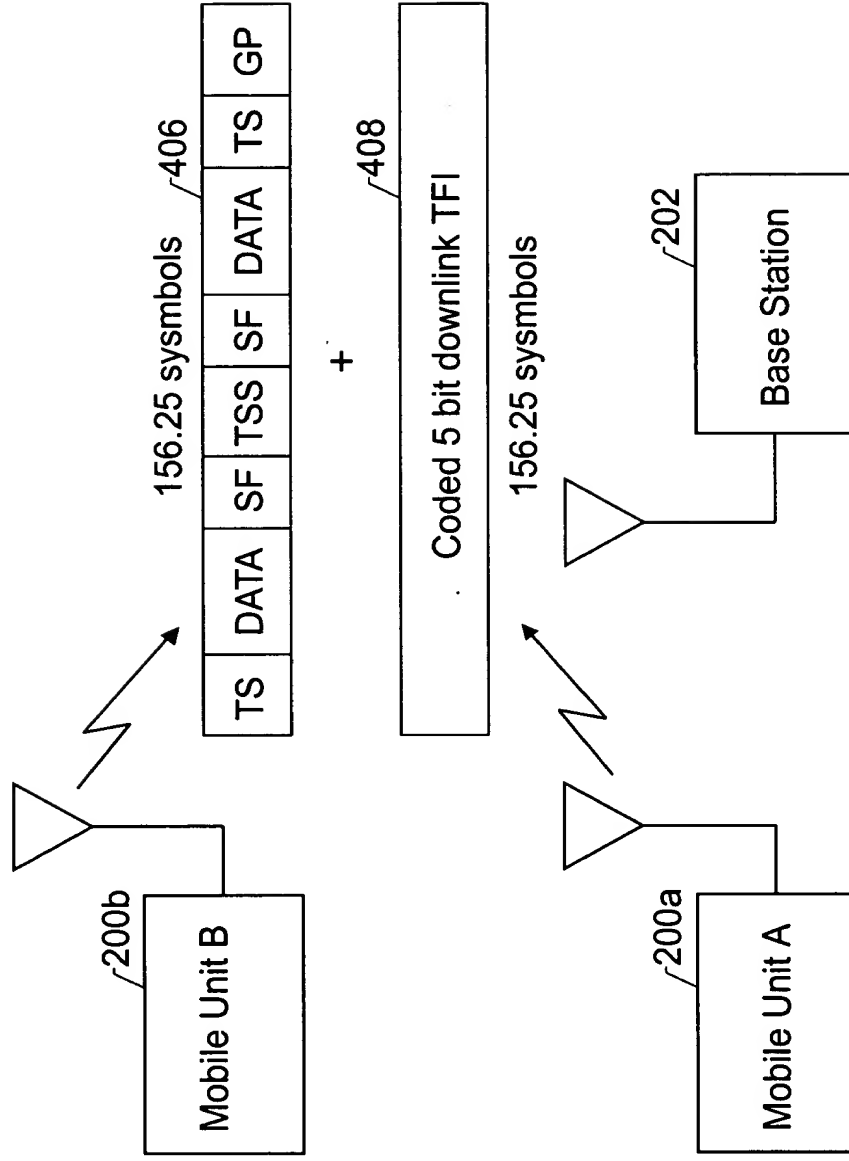


FIG. 5

RTTUAM

500

502

< RTFACCH Uplink Assignment Message

Message content > :: =

< TFI: bit (5) >

{0|1 < Uplink_TFI_ASSIGNMENT: bit (5) >}

< TSC: bit (3) >

< ARFCN: bit (10) >

< TIMESLOT_ALLOCATION: bit (8) >

< padding bits >;

FIG. 6

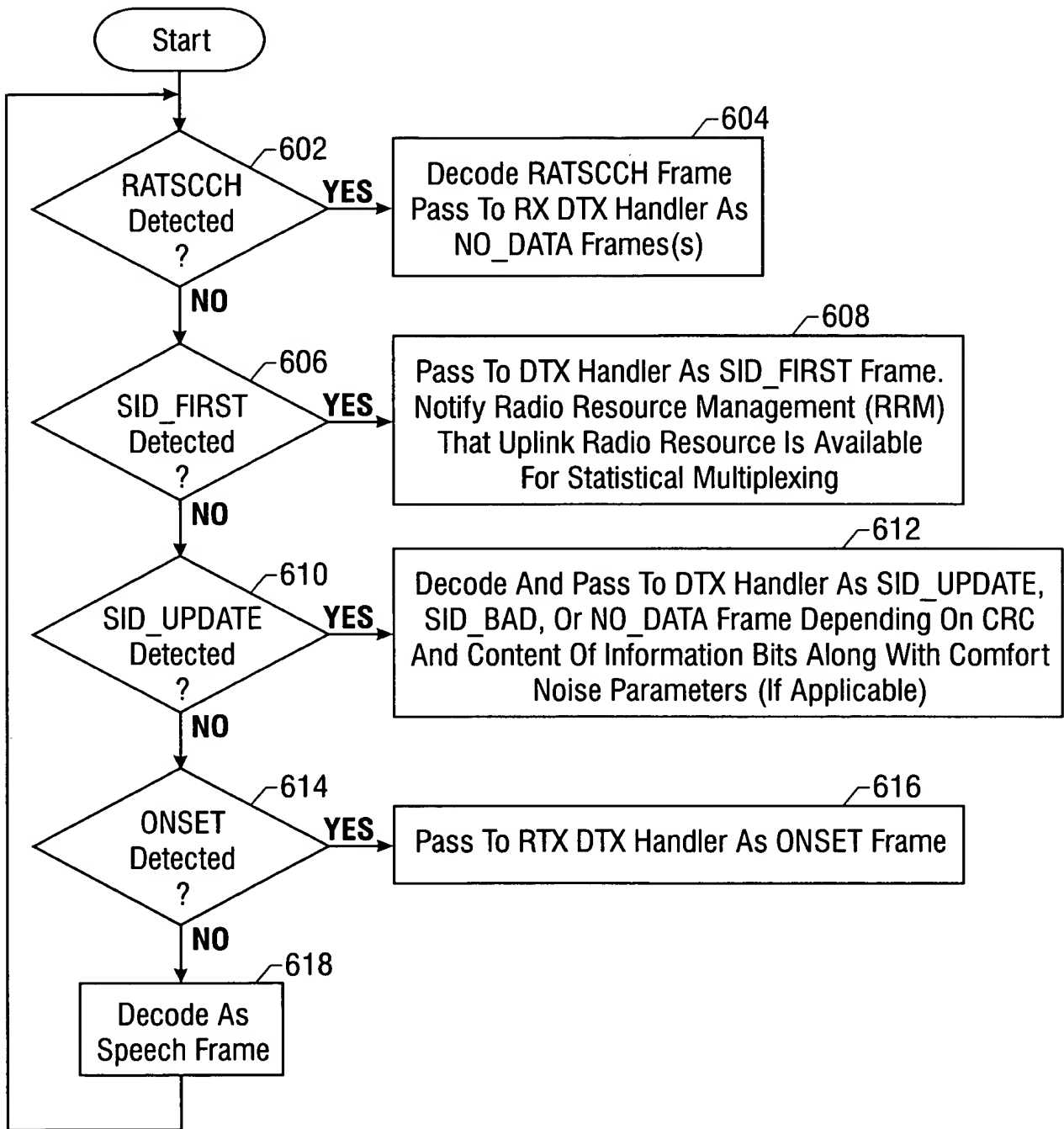


FIG. 7

00443400

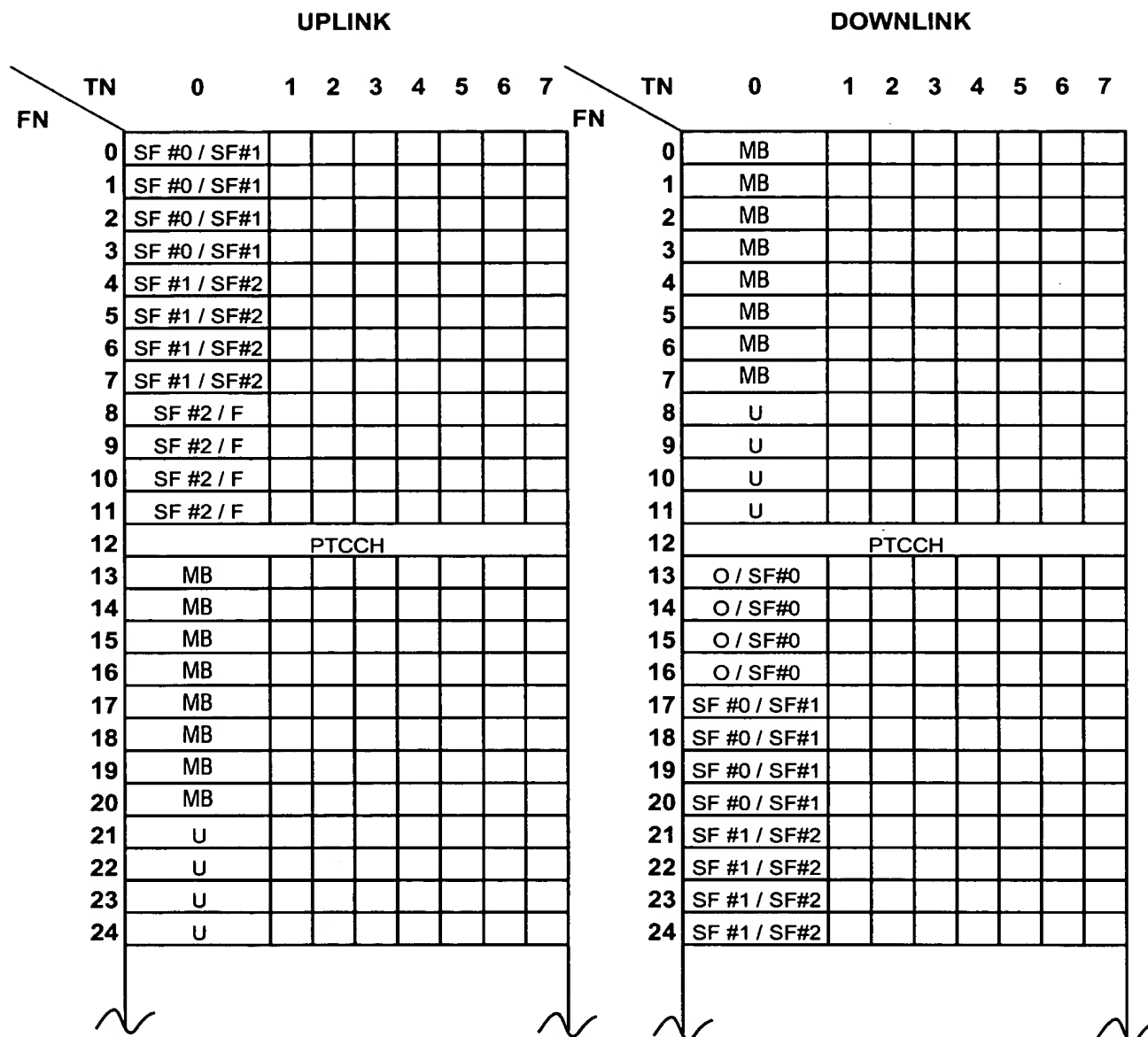


FIG. 8A

25	IDLE							
26	MB							
27	MB							
28	MB							
29	MB							
30	MB							
31	MB							
32	MB							
33	MB							
34	U							
35	U							
36	U							
37	U							
38	PTCCH							
39	MB							
40	MB							
41	MB+R							
42	MB							
43	O / SF#0							
44	O / SF#0							
45	O / SF#0							
46	O / SF#0							
47	SF #0 / SF#1							
48	SF #0 / SF#1							
49	SF #0 / SF#1							
50	SF #0 / SF#1							
51	IDLE							

700

25	IDLE							
26	SF #2 / SF#3							
27	SF #2 / SF#3							
28	SF #2 / SF#3							
29	SF #2 / SF#3							
30	SF #3 / SF#4							
31	SF #3 / SF#4							
32	SF #3 / SF#4							
33	SF #3 / SF#4							
34	SF #4 / F							
35	SF #4 / F							
36	SF #4 / F							
37	SF #4 / F							
38	PTCCH							
39								
40								
41								
42	A							
43								
44								
45								
46								
47	U							
48	U							
49	U							
50	U							
51	IDLE							

702

Minimum
Delay

FIG. 8B

FIG. 9A

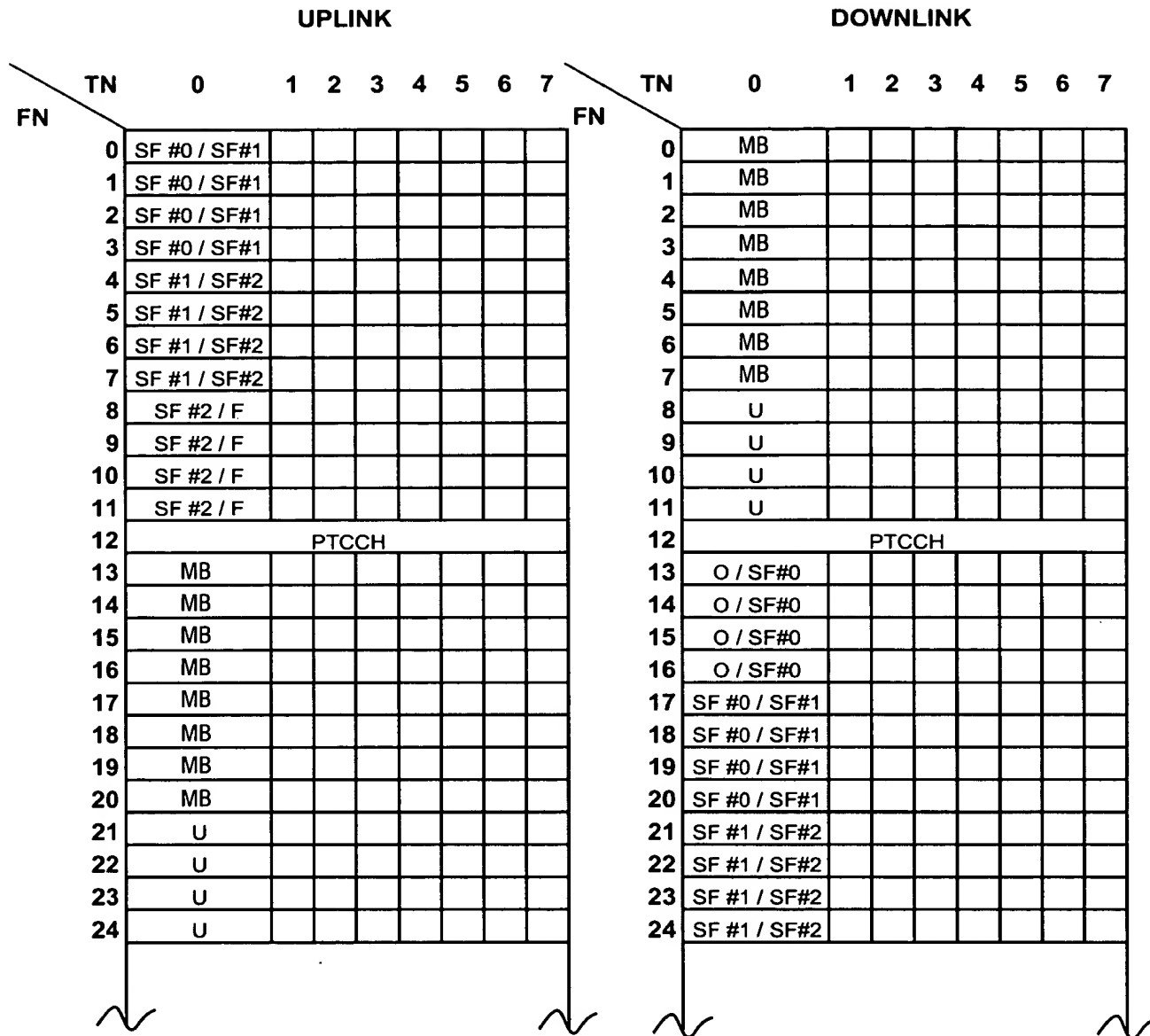


FIG. 9A

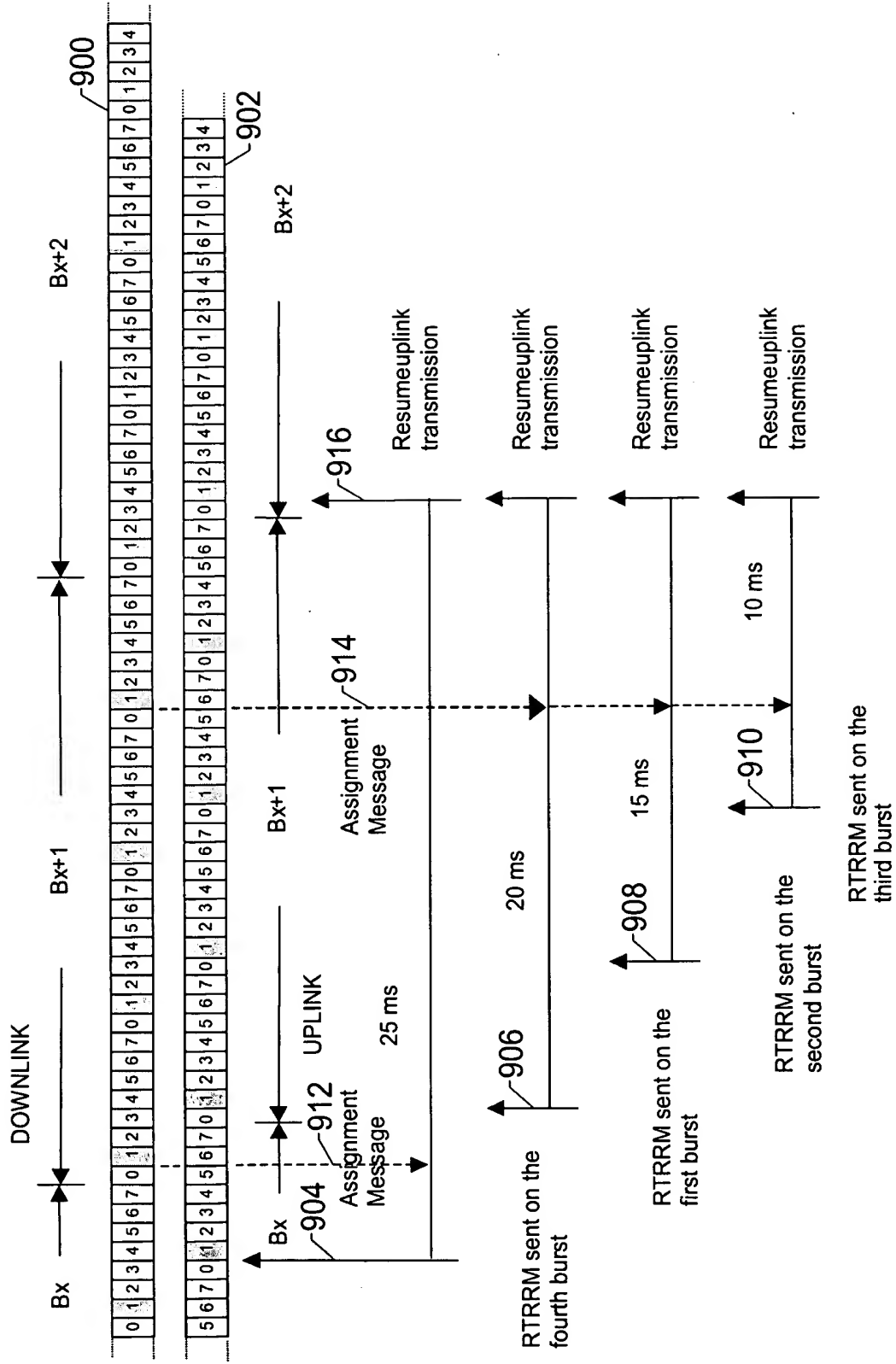


FIG. 10

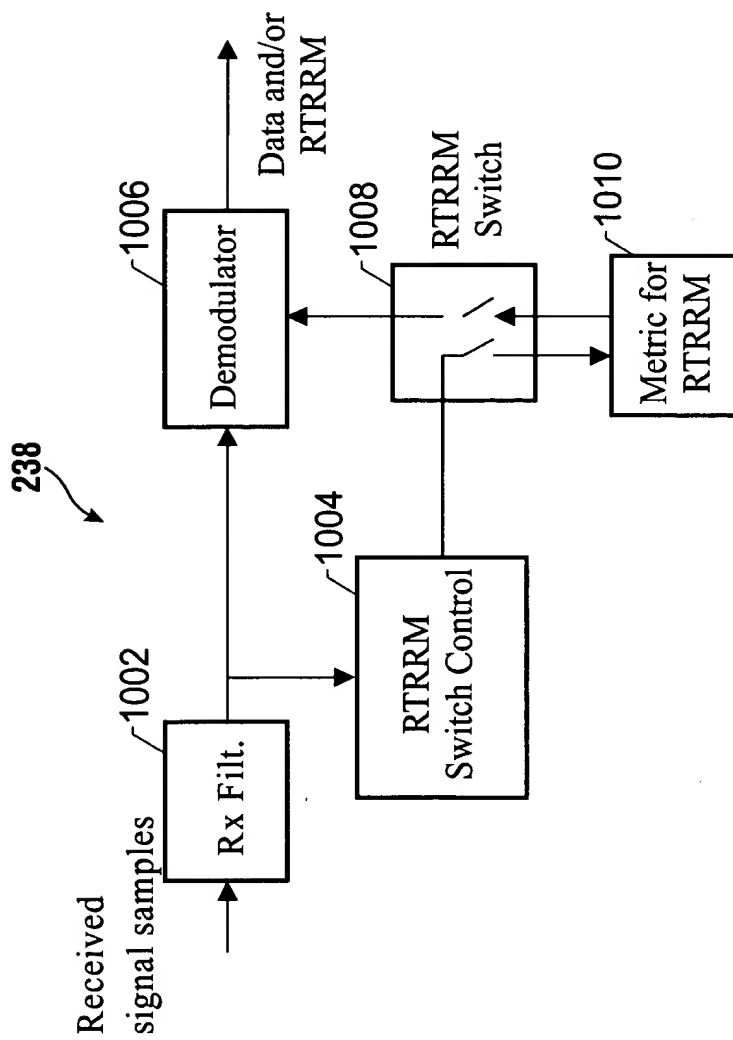


FIG. 11